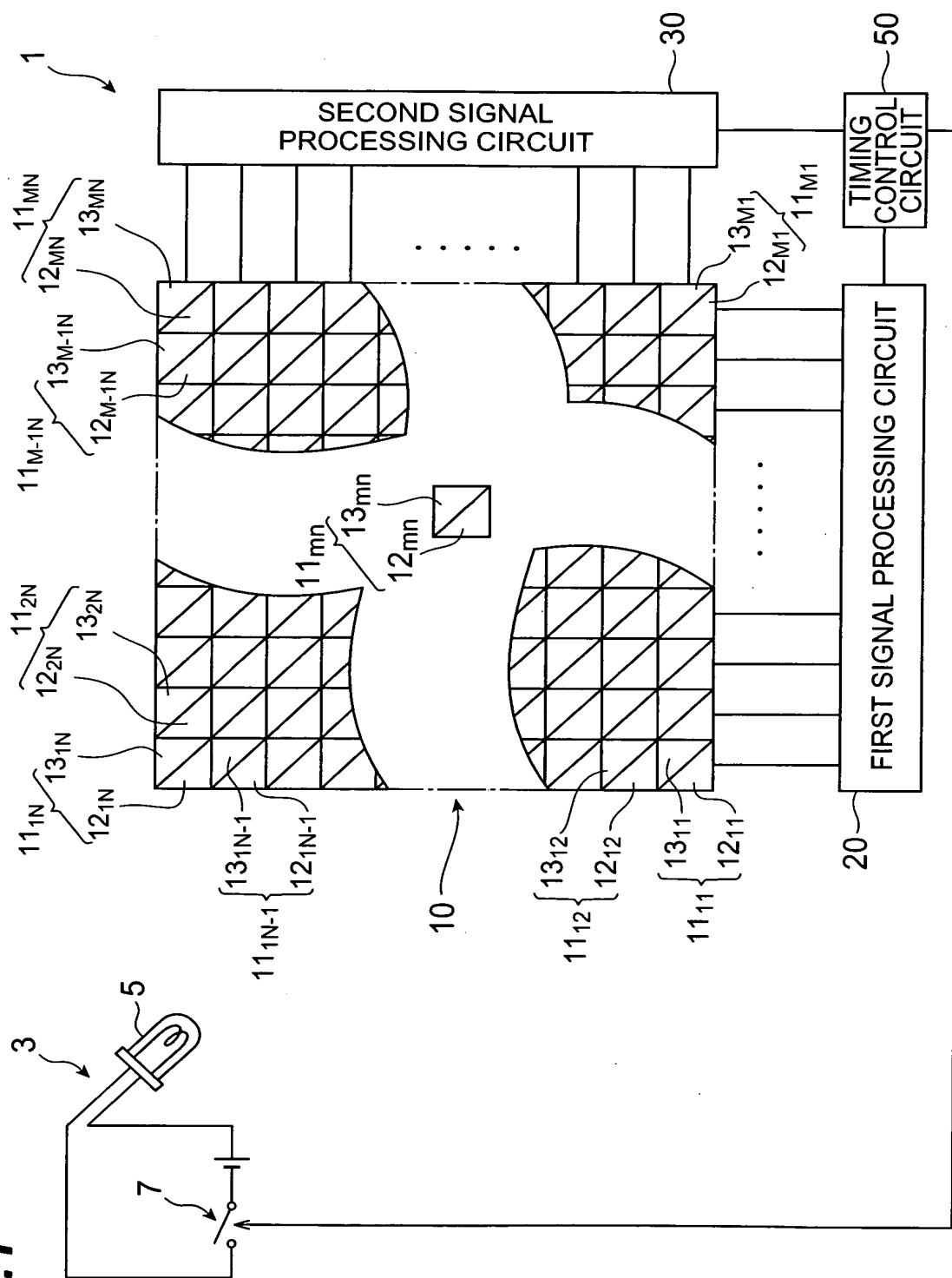
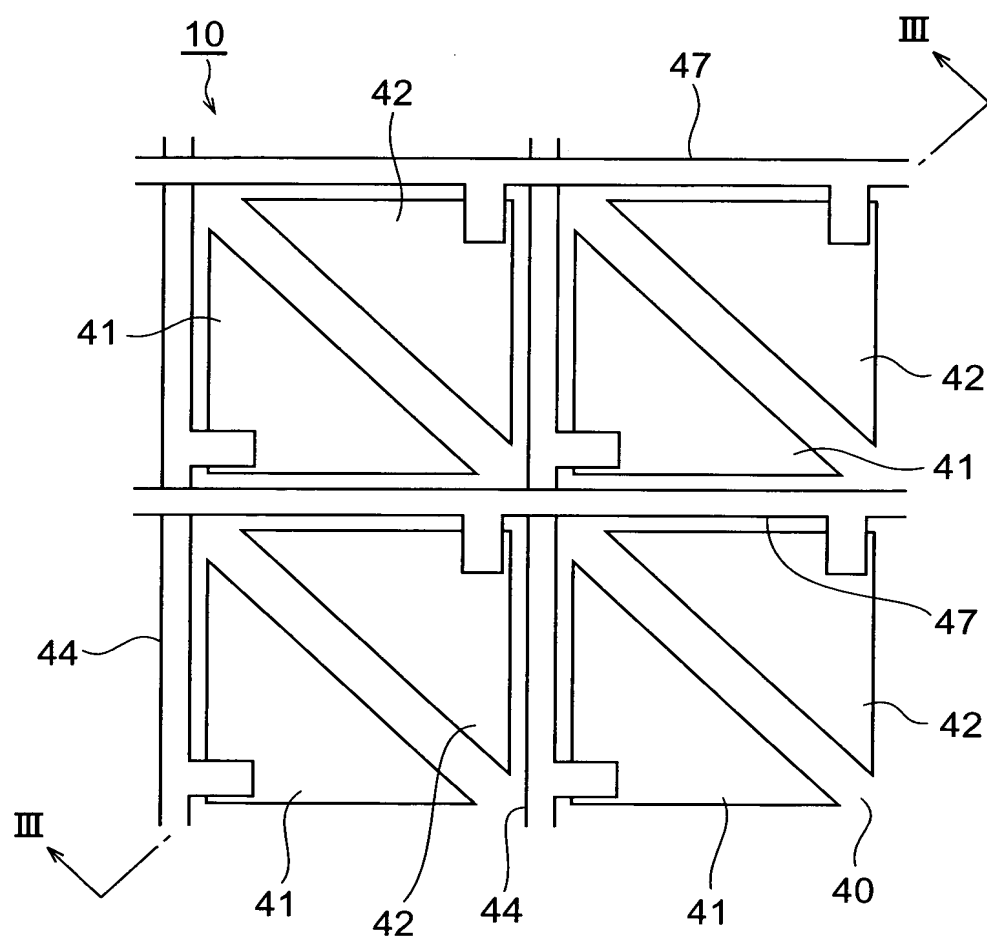
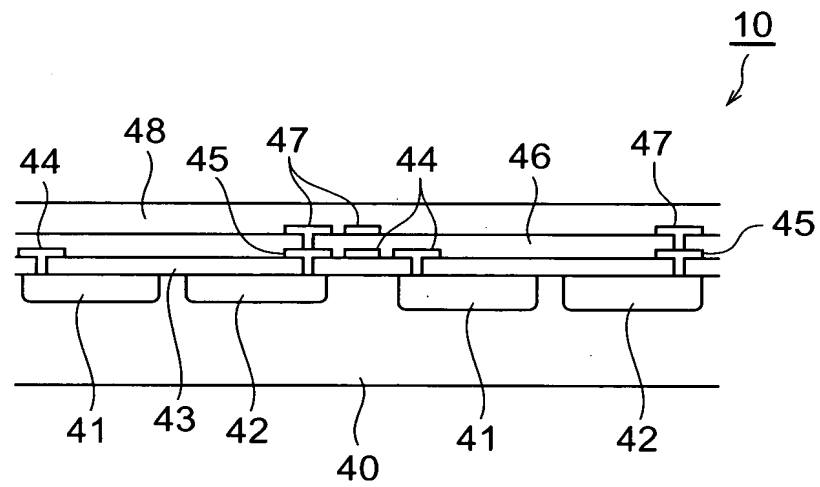


**Fig. 1**



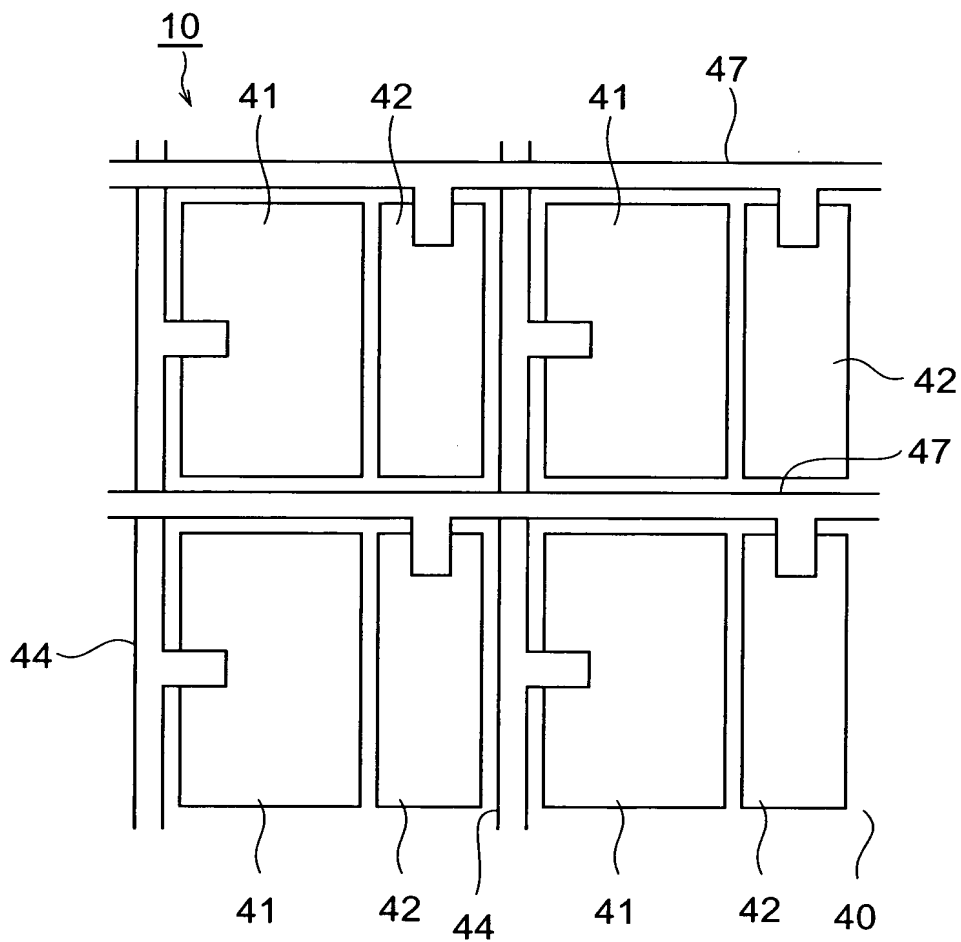
**Fig.2**

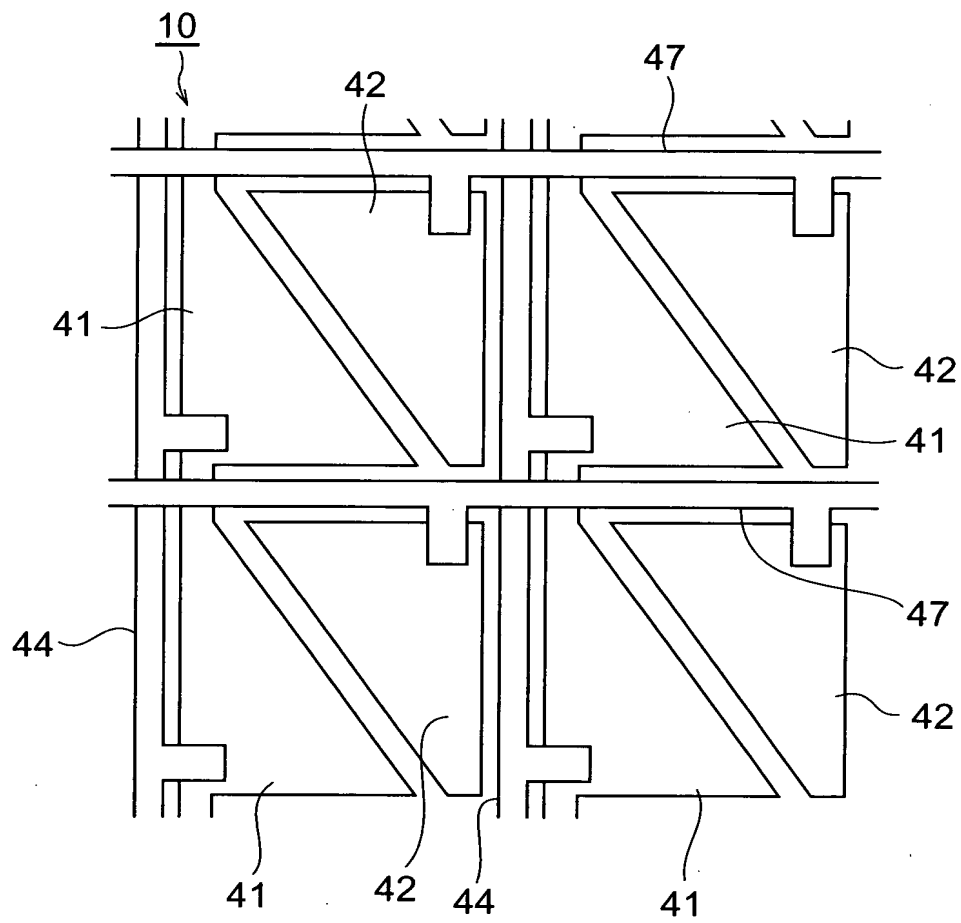
**Fig.3**

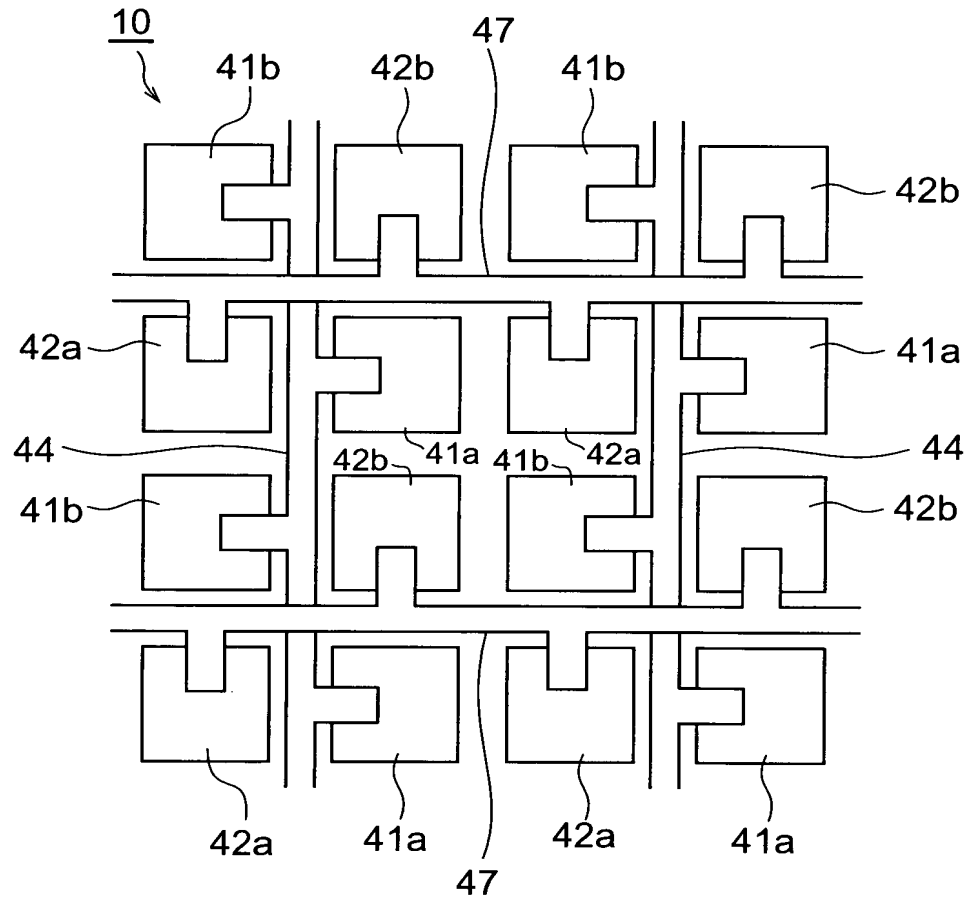


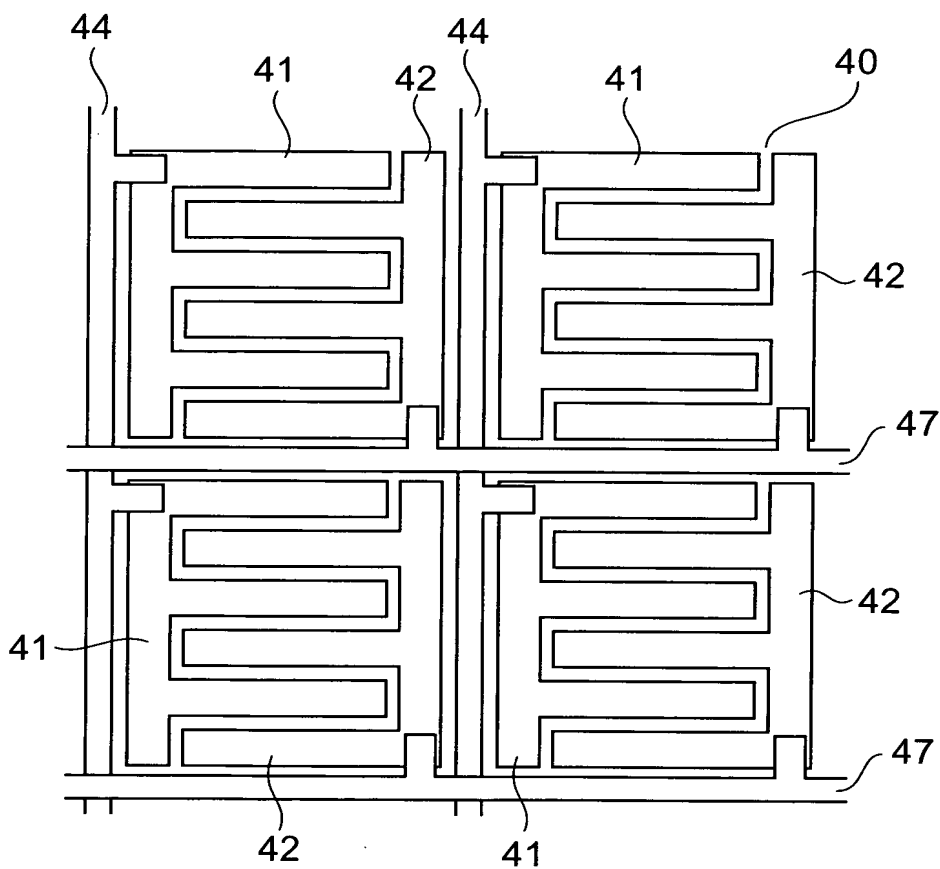
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**Fig.4**

**Fig.5**

**Fig.6**

**Fig.7**

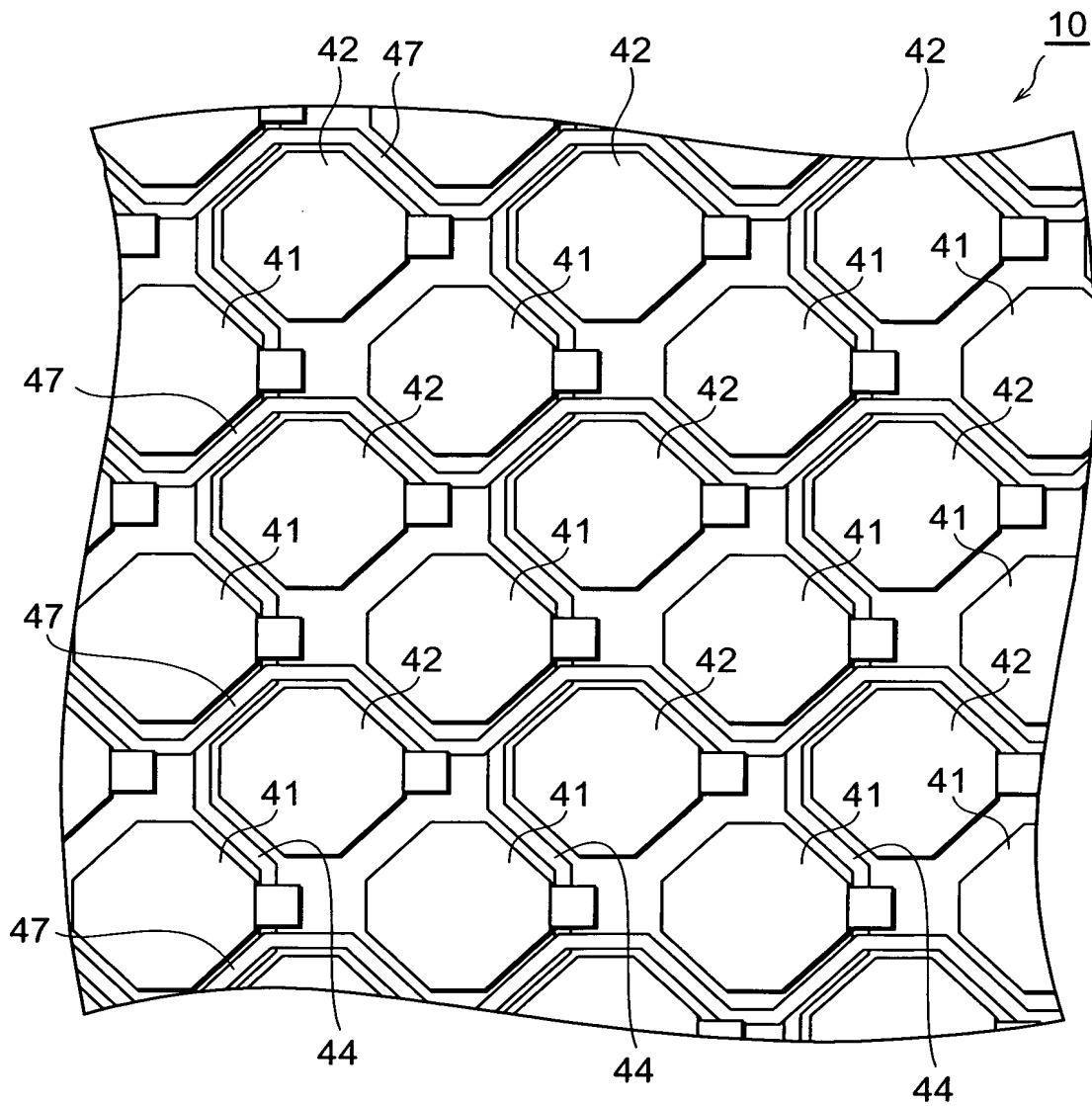
**Fig.8**



Fig.9

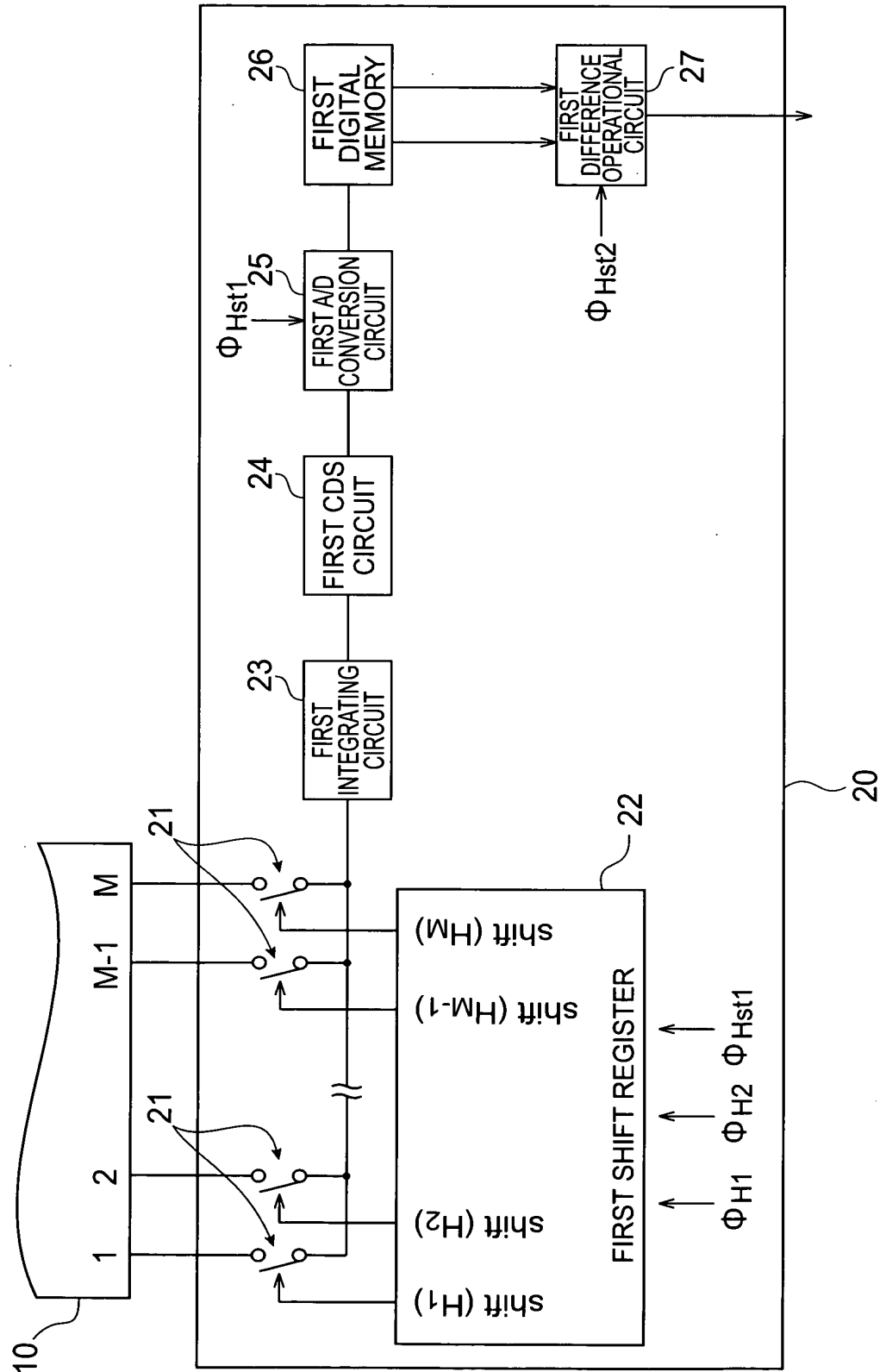
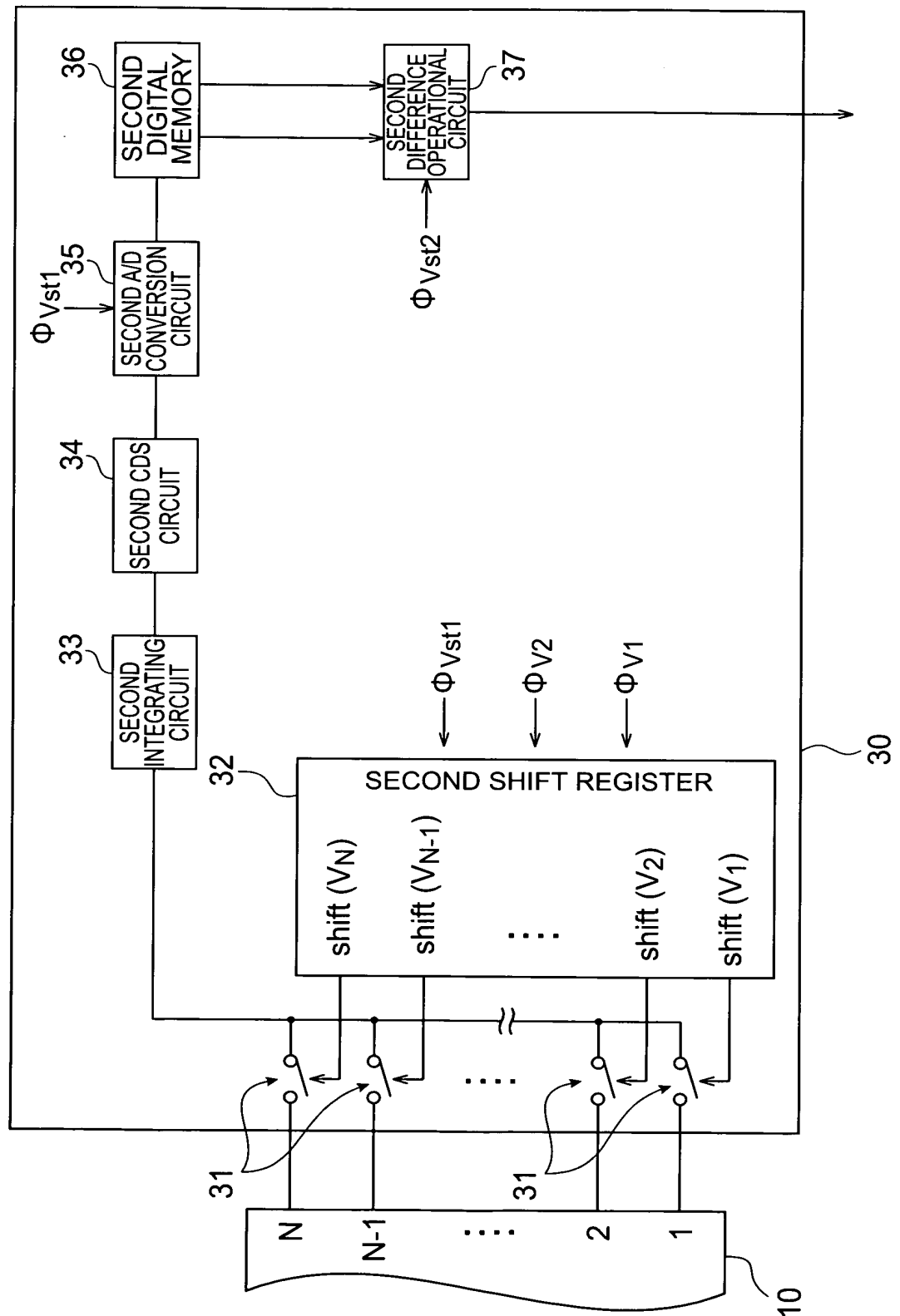
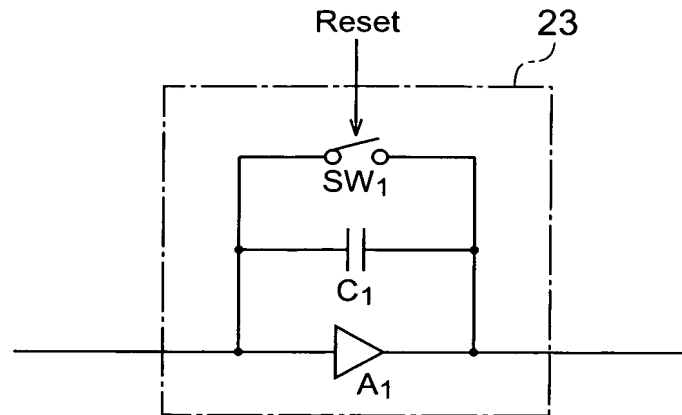
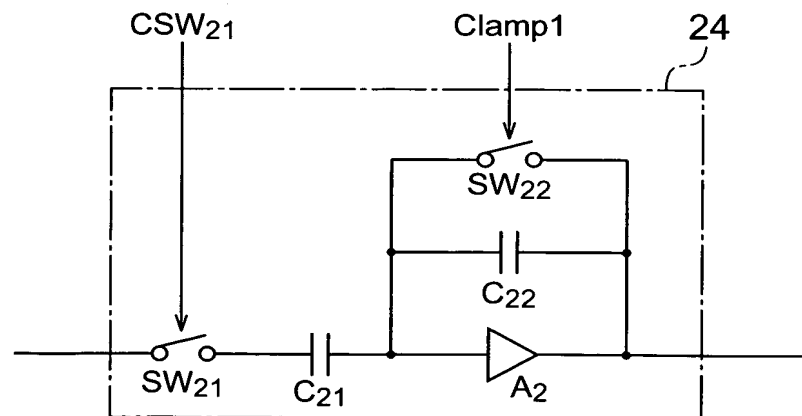


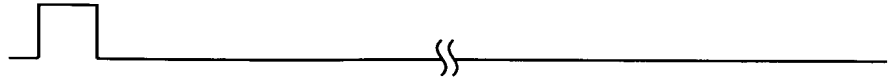
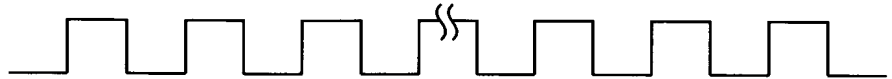
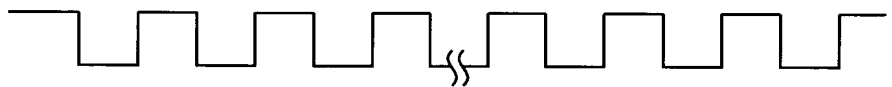
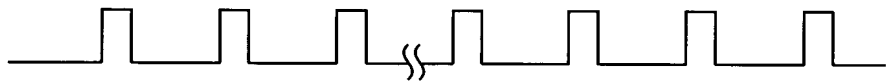
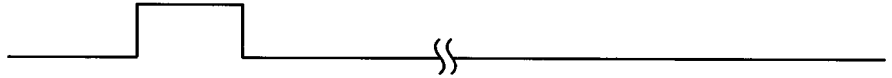
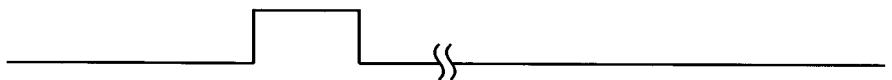
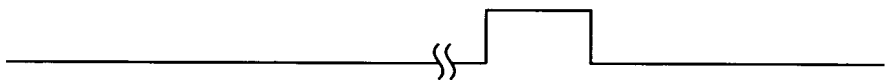
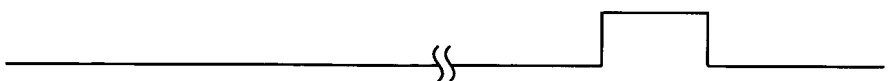
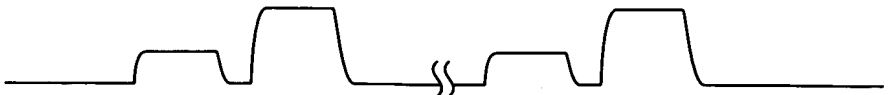
Fig.10

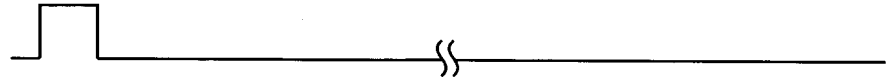
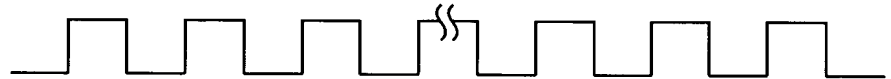
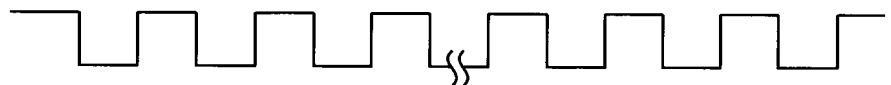
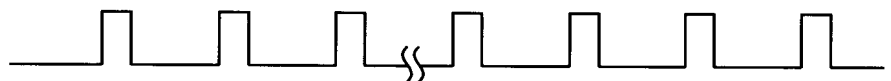
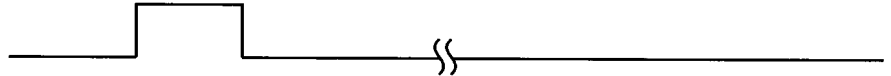
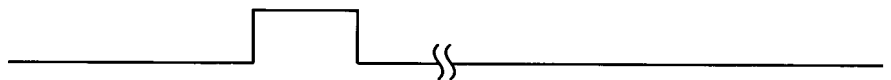
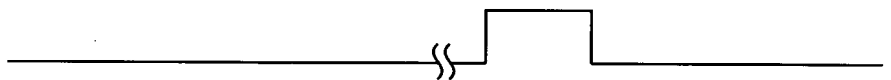
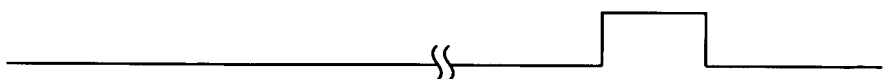
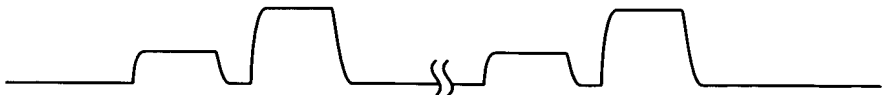


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**Fig.11****Fig.12**

**Fig.13A** $\Phi_{Hst1}$ **Fig.13B** $\Phi_{H1}$ **Fig.13C** $\Phi_{H2}$ **Fig.13D** $\Phi_{Hreset}$ **Fig.13E**shift( $H_1$ )**Fig.13F**shift( $H_2$ )**Fig.13G**shift( $H_{M-1}$ )**Fig.13H**shift( $H_M$ )**Fig.13I**OUTPUT OF  
THE FIRST  
INTEGRATING  
CIRCUIT

**Fig.14A** $\Phi_{Vst1}$ **Fig.14B** $\Phi_{V1}$ **Fig.14C** $\Phi_{V2}$ **Fig.14D** $\Phi_{Vreset}$ **Fig.14E**shift( $V_1$ )**Fig.14F**shift( $V_2$ )**Fig.14G**shift( $V_{N-1}$ )**Fig.14H**shift( $V_N$ )**Fig.14I**OUTPUT OF  
THE SECOND  
INTEGRATING  
CIRCUIT

**Fig.15A** $\Phi_{Hst1}$  **Fig.15B** $\Phi_{Hst2}$  **Fig.15C**

LED

**Fig.15D**OUTPUT OF  
THE FIRST  
A/D  
CONVERSION  
CIRCUIT **Fig.15E**OUTPUT OF  
THE FIRST  
DIFFERENCE  
OPERATIONAL  
CIRCUIT **Fig.16A** $\Phi_{Vst1}$  **Fig.16B** $\Phi_{Vst2}$  **Fig.16C**

LED

**Fig.16D**OUTPUT OF  
THE SECOND  
A/D  
CONVERSION  
CIRCUIT **Fig.16E**OUTPUT OF  
THE SECOND  
DIFFERENCE  
OPERATIONAL  
CIRCUIT

**Fig.17**

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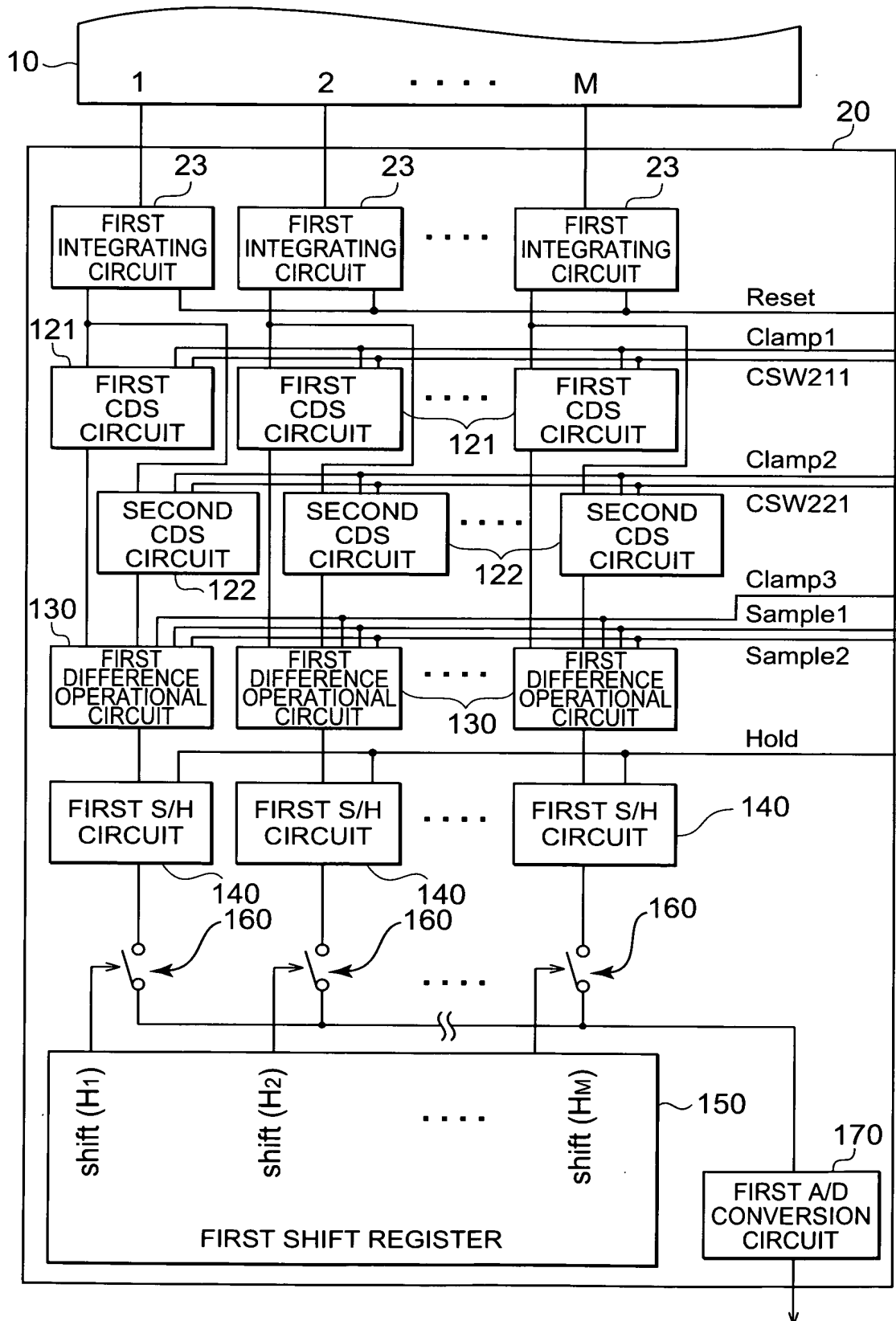
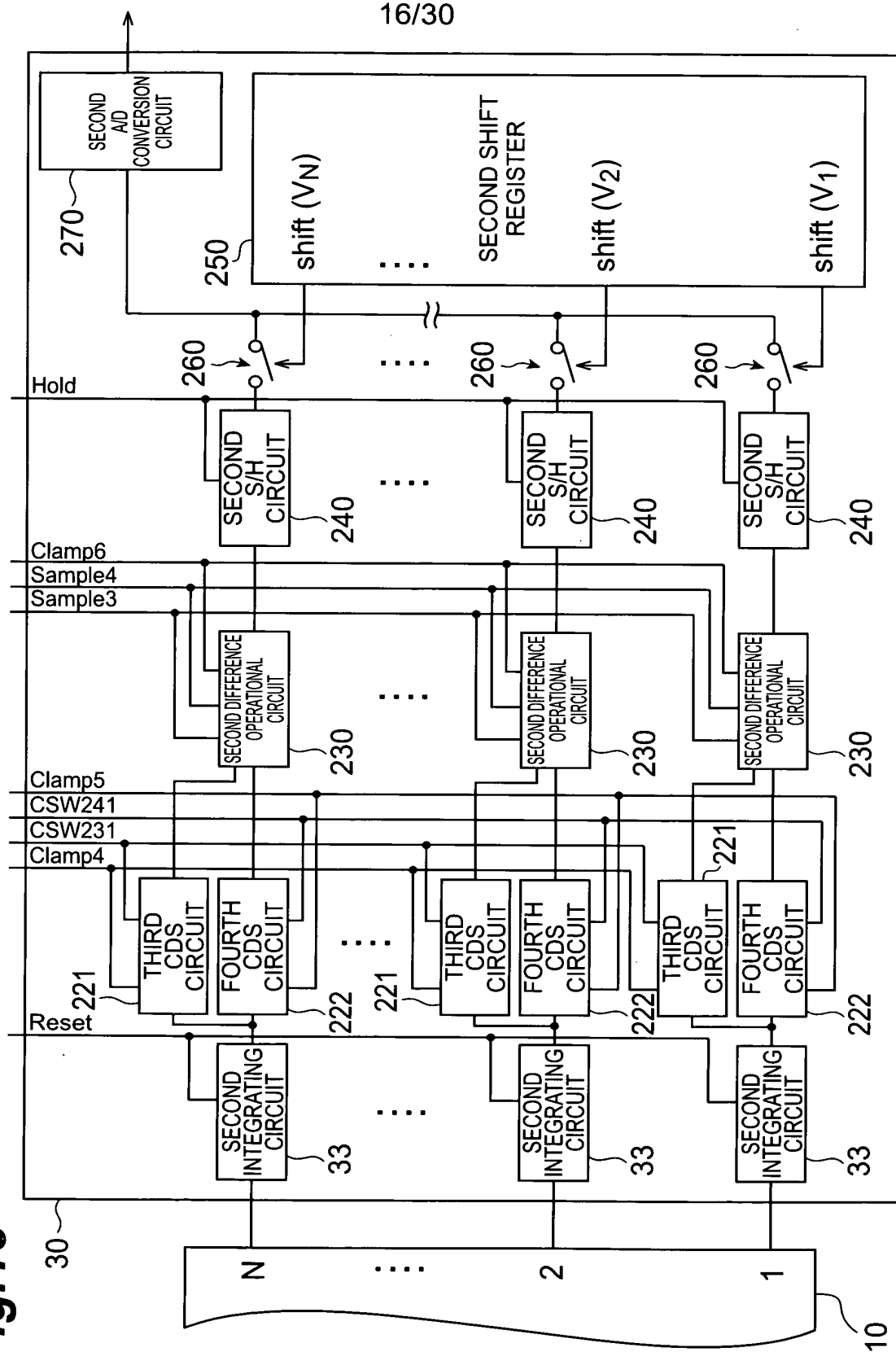
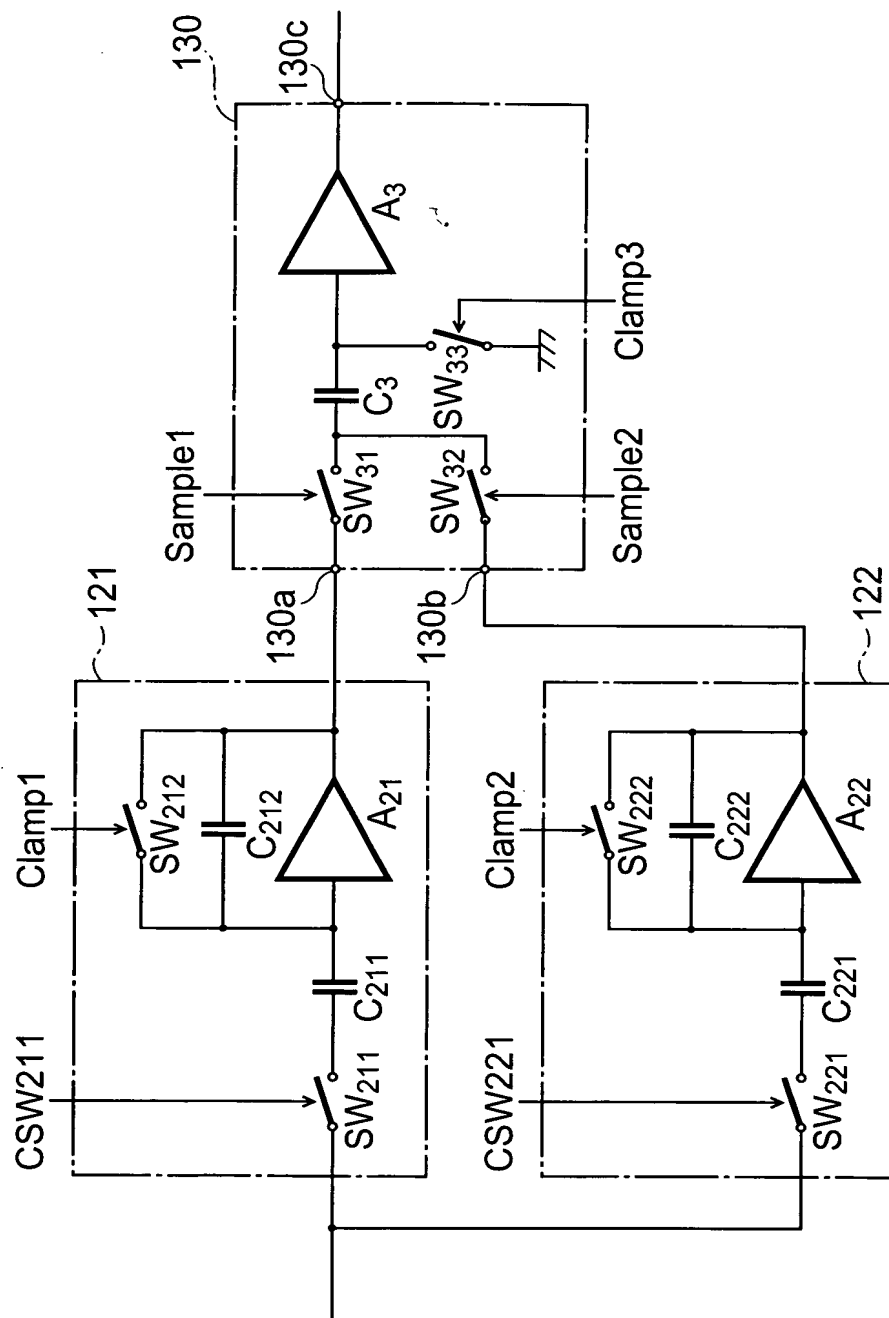


Fig.18

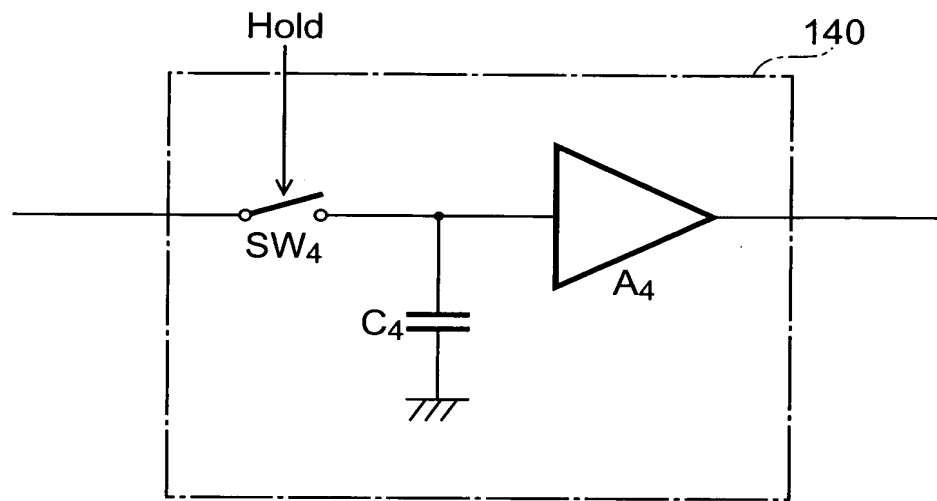




**Fig.19**

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**Fig.20**

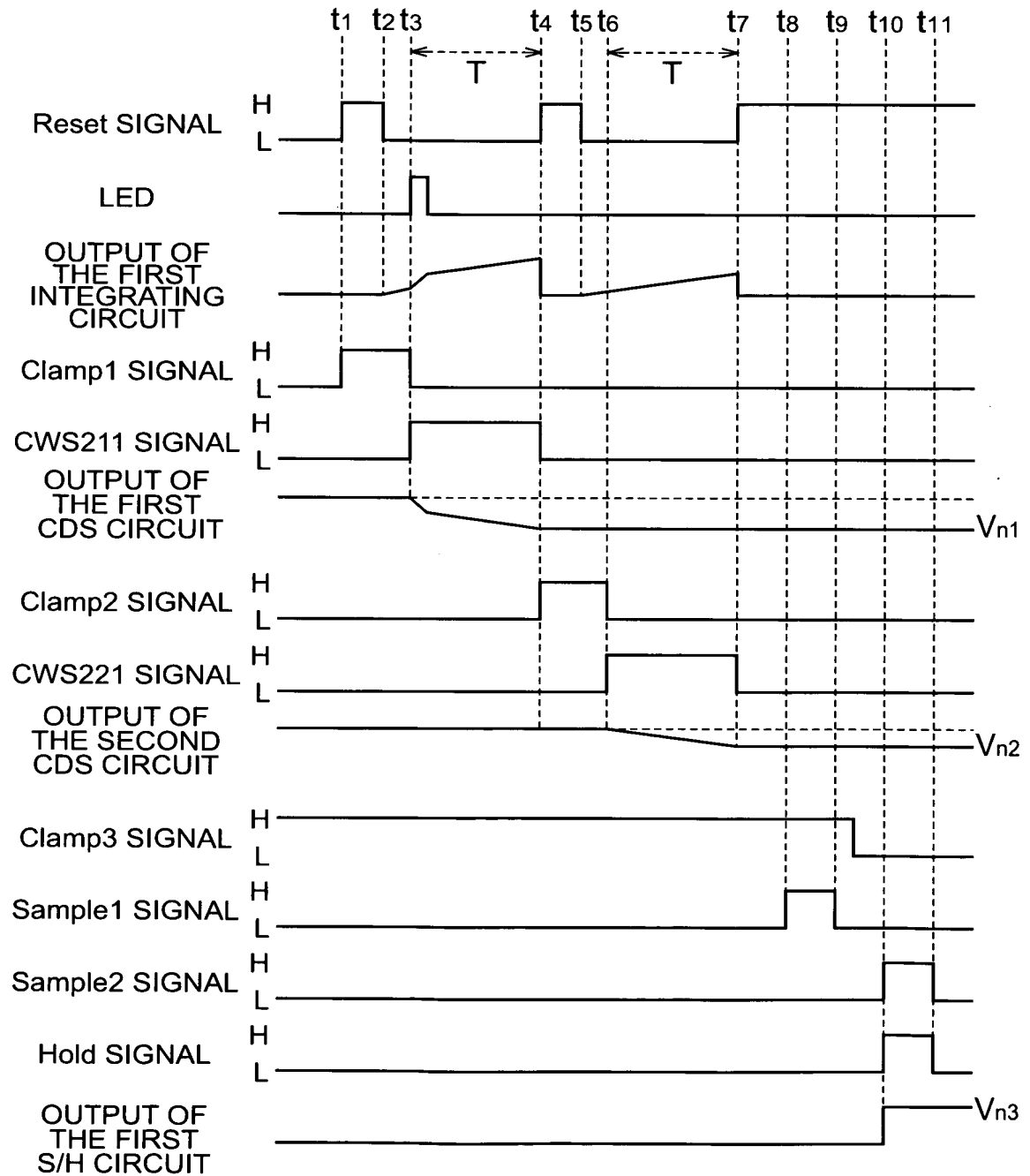
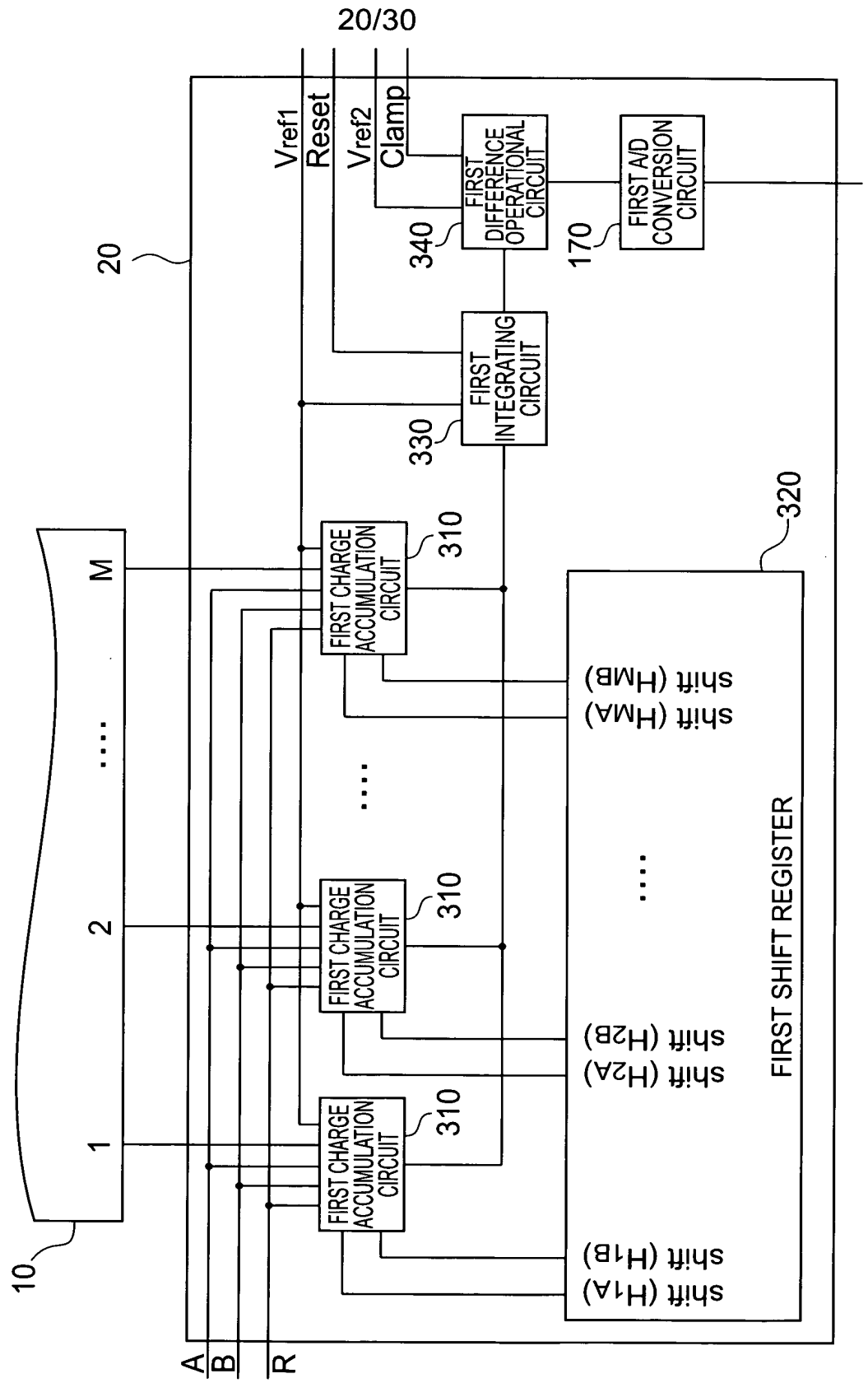
**Fig.21**

Fig.22



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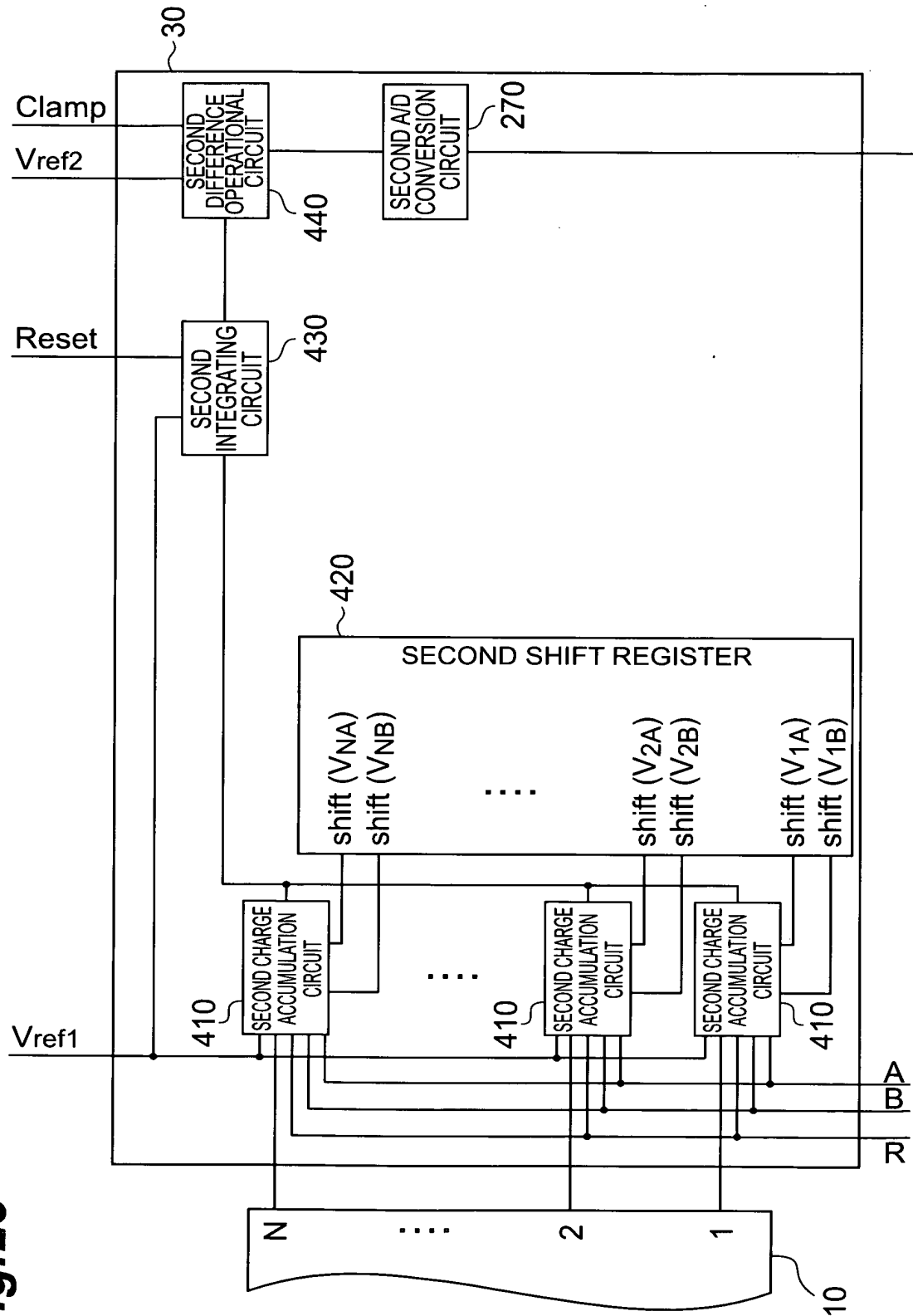
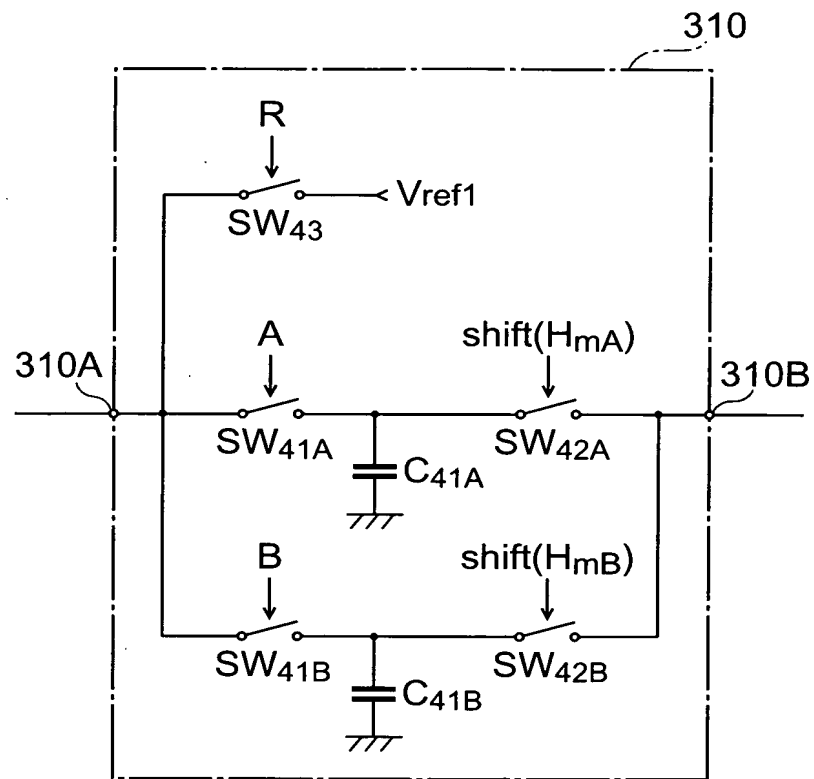
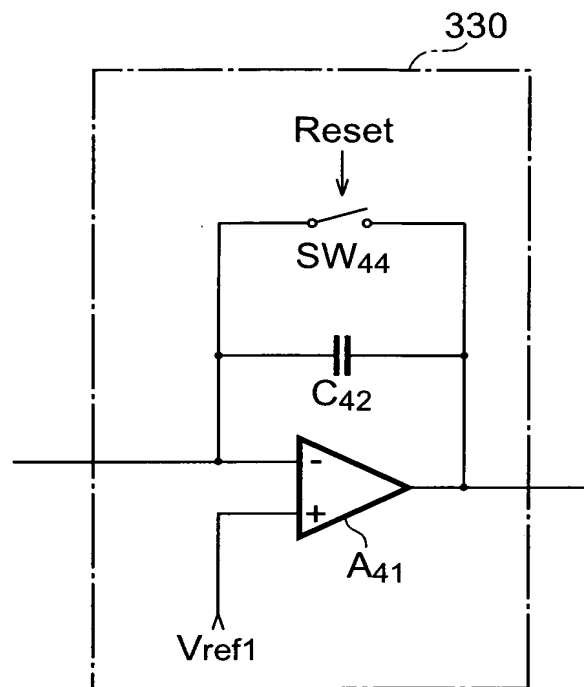


Fig. 23

**Fig.24**

**Fig.25**

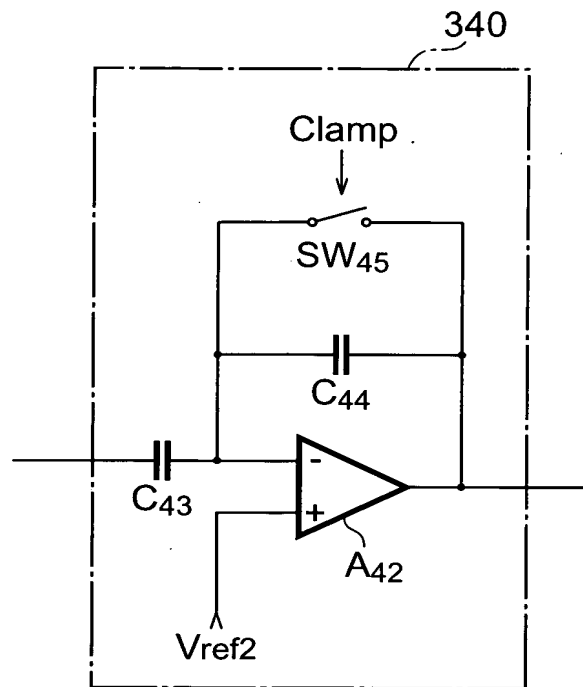
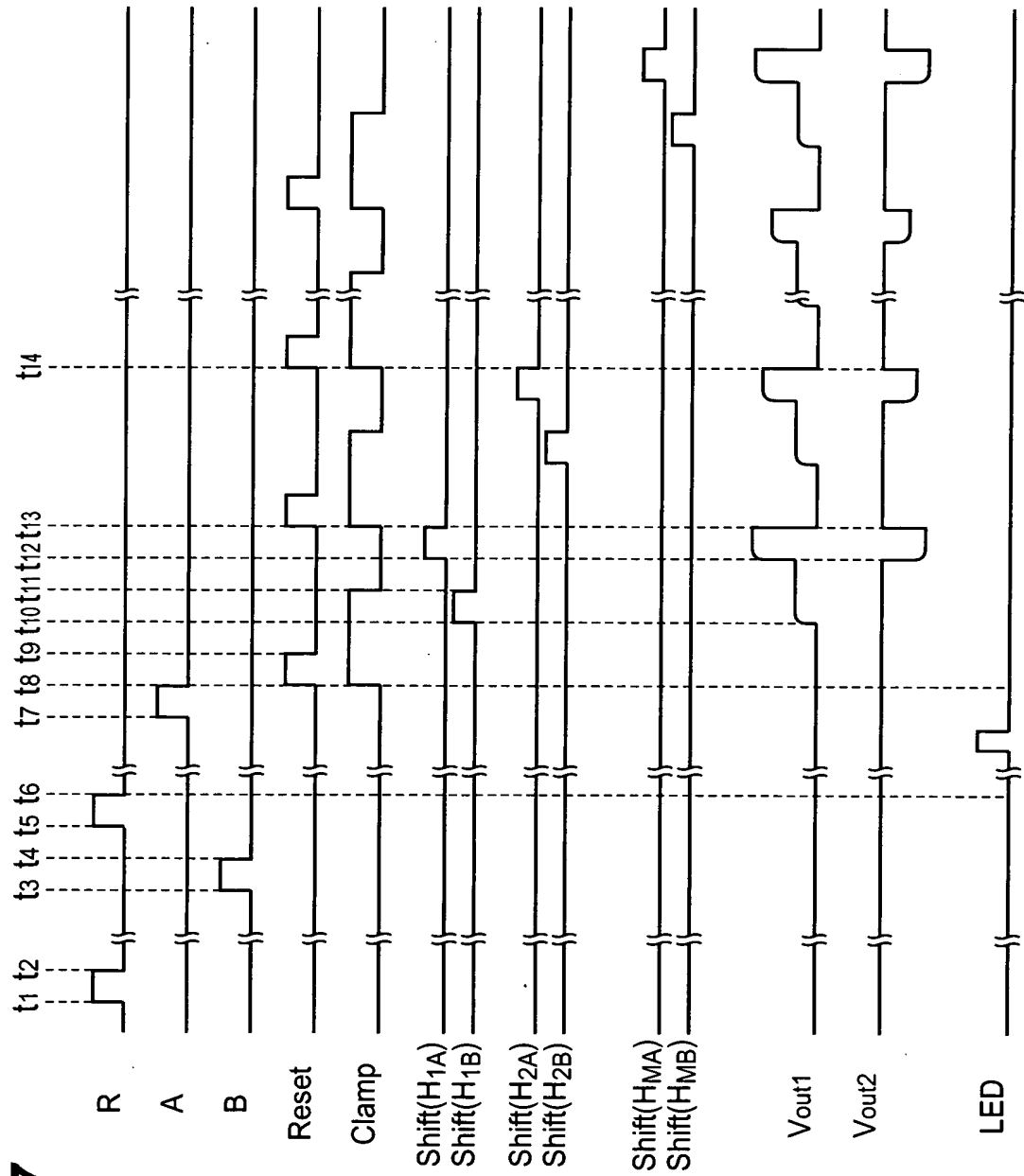
**Fig.26**



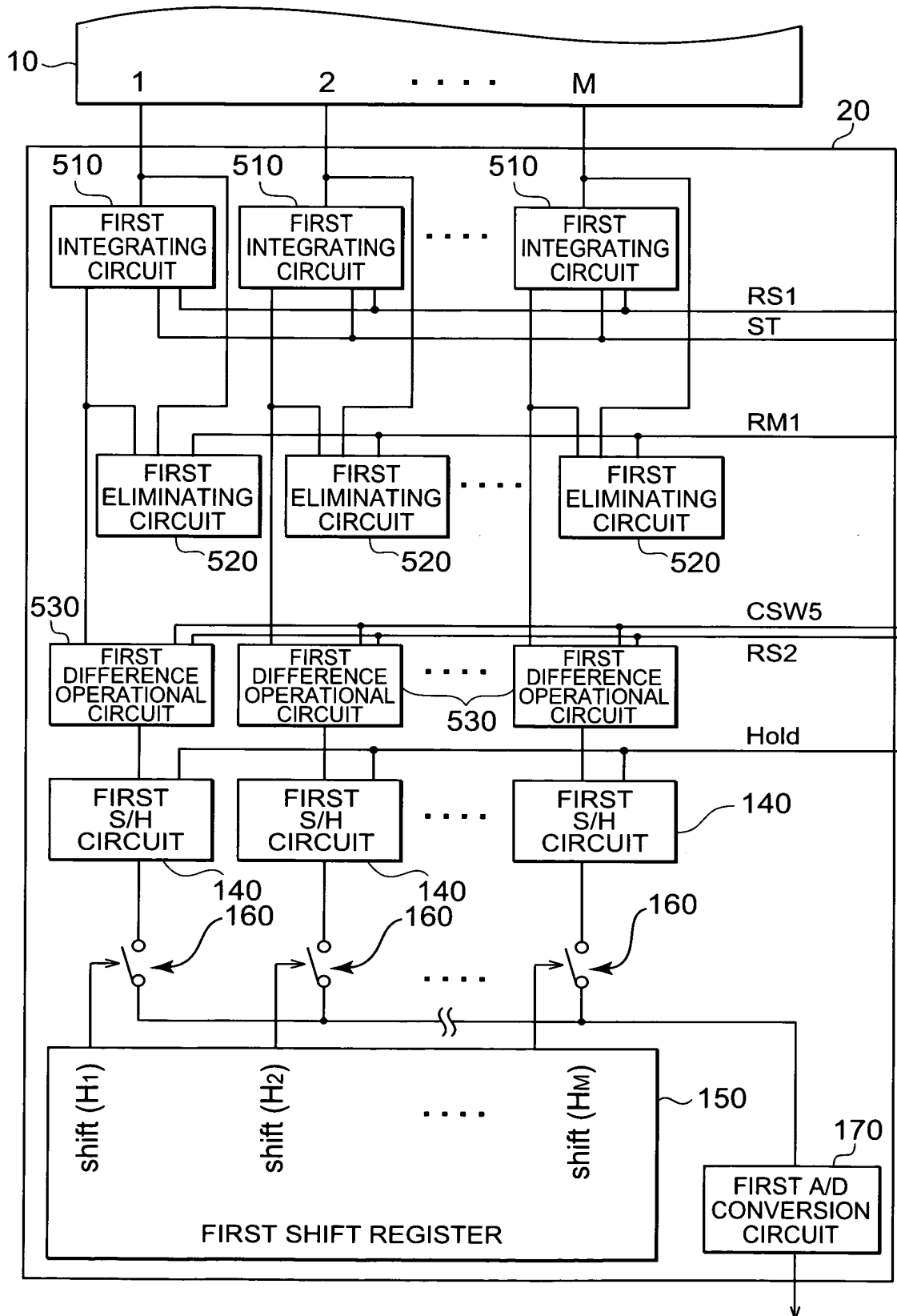
Fig.27



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**Fig.28**

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Fig. 29

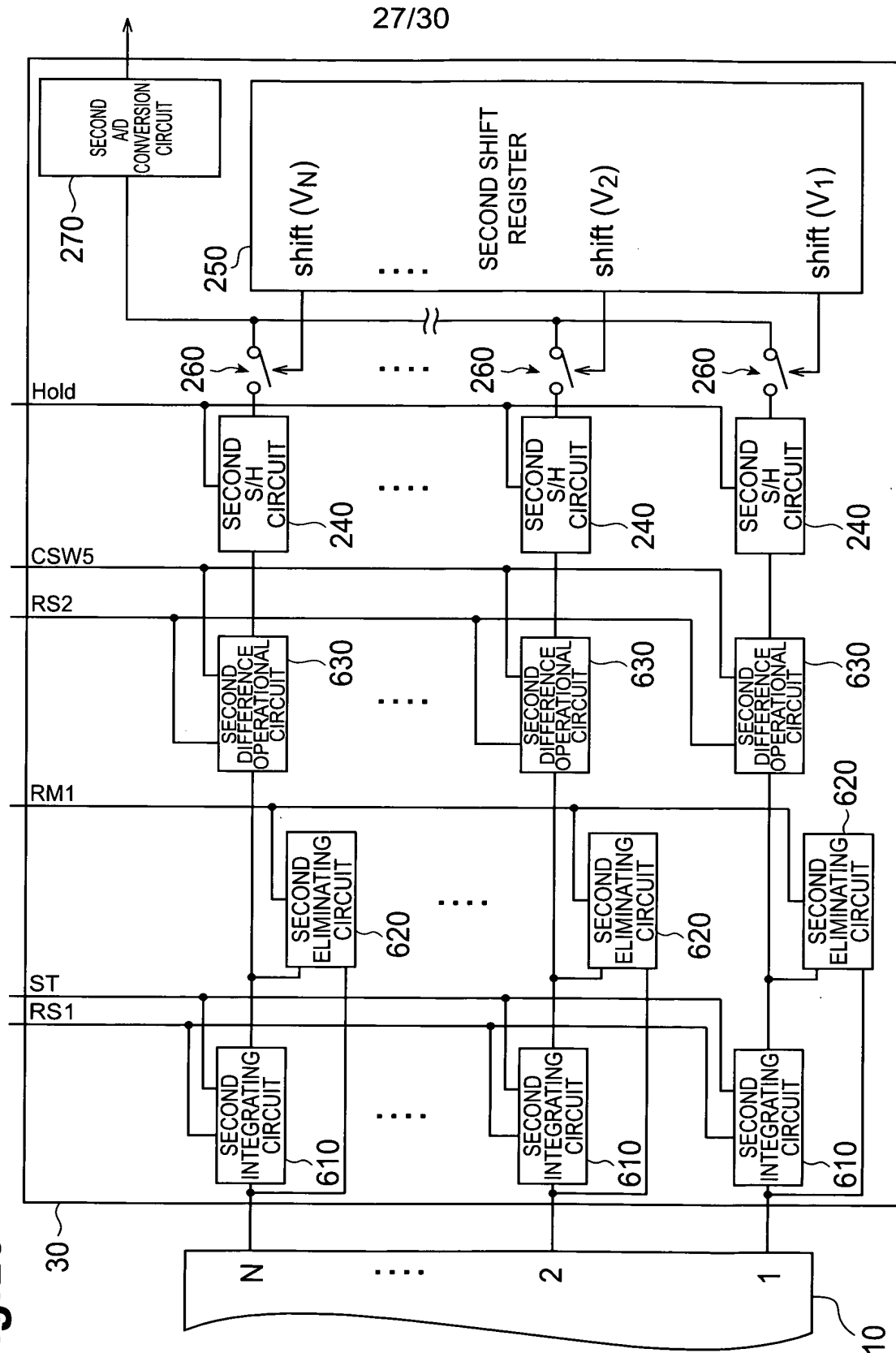
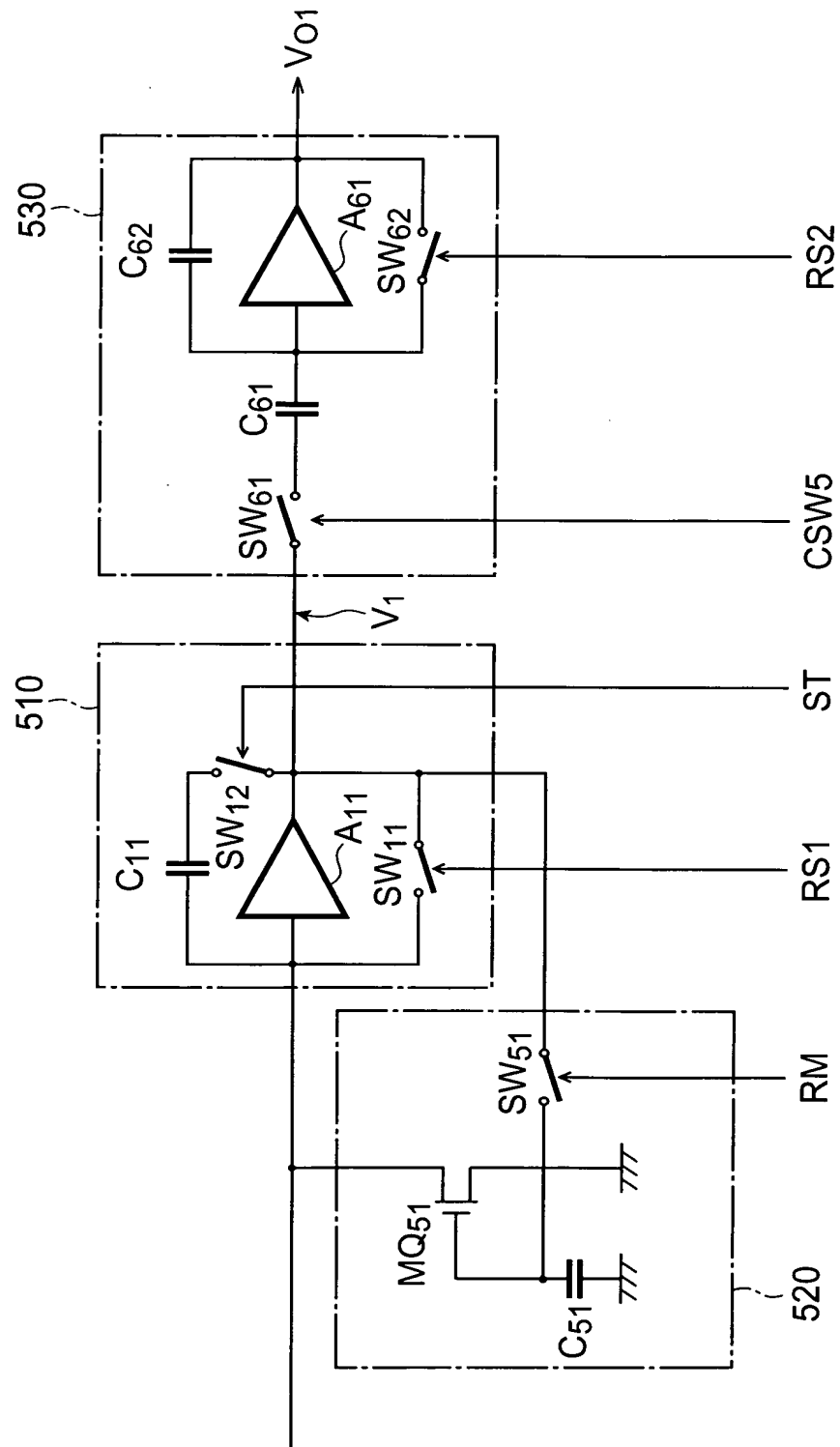


Fig.30



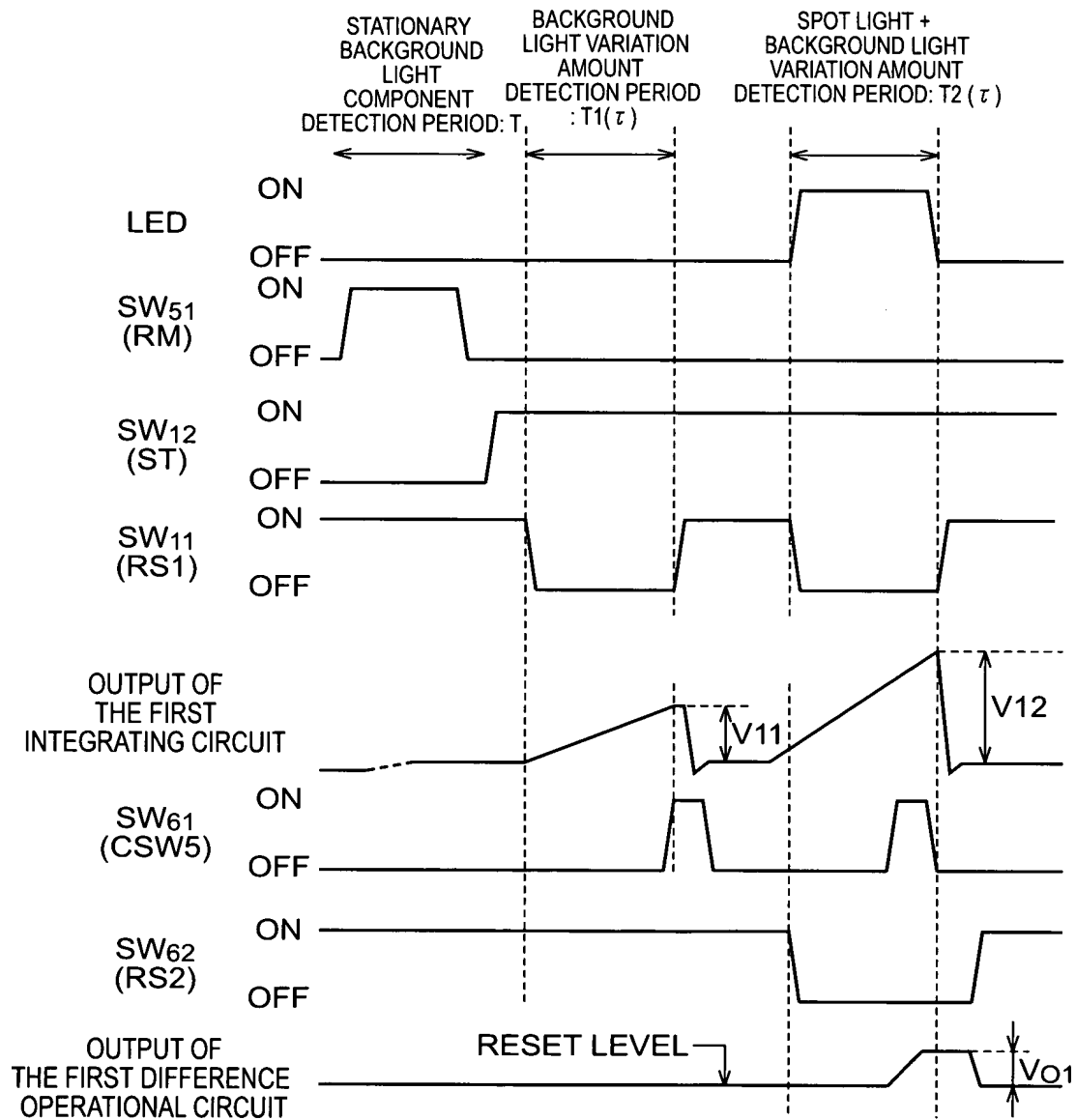
**Fig.31**

Fig.32

